UWI
ST. AUGUSTINE CAMPUS

ARTIFICIAL INTELLIGENCE INNOVATION CENTRE

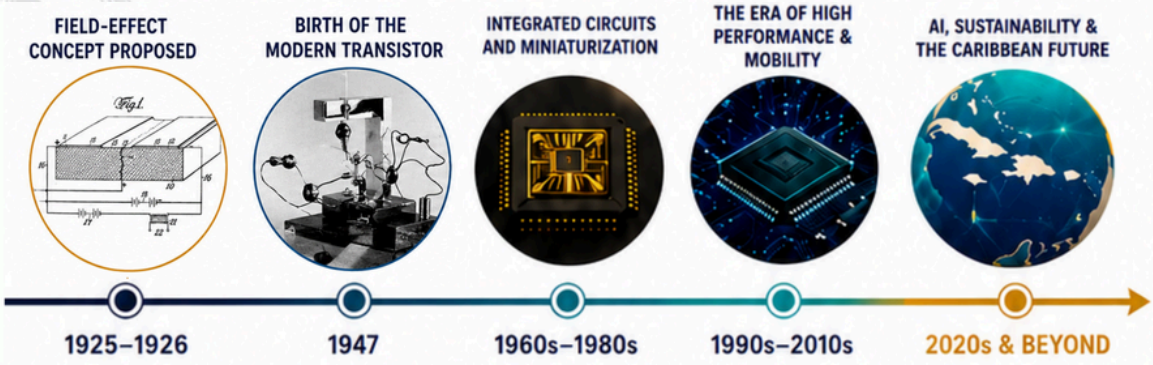
ELECTRON DEVICES SOCIETY
STUDENT BRANCH CHAPTER
TRINIDAD AND TOBAGO

IEEE
Advancing Technology for Humanity

FET-100

LAUNCHING SEMICONDUCTOR ELECTRON DEVICES TECHNOLOGIES IN THE CARIBBEAN

CELEBRATING 100 YEARS OF THE FIELD EFFECT TRANSISTOR



OCTOBER 5-6, 2026

9:00 AM EST

UWI, ST. AUGUSTINE CAMPUS, TRINIDAD AND TOBAGO

Bringing together academia, government and industry to explore semiconductor education, research, workforce development and investment opportunities for the Caribbean.

- EXPERT LECTURES & PANEL DISCUSSION
- INDUSTRIAL EXHIBITION
- POSTER COMPETITION
- NETWORKING OPPORTUNITIES

#FET100Caribbean

DAY 1
OCTOBER 5TH 2026
TECHNICAL PROGRAM

TIME	SPEAKER/SESSION	TOPIC/DETAILS
8:30 - 9:00 AM	REGISTRATION	
9:00 - 9:10 AM	OPENING REMARKS	Welcome address and session overview.
9:10 - 9:40 AM	Fernando Guarin	100th Anniversary of the Transistor with an Overview of the Semiconductor Industry Today and the Opportunities for Latin America
9:40 - 10:10 AM	Arokia Nathan	The Thin Film Transistor - Evolution and Applications
10:10 - 10:40 AM	Durga Misra	Engineering Energy Efficient Switching Devices for AI Hardware
10:40 - 11:10 AM	Victor Grimblatt	Synopsys and its Role in the Development of Design Centres in Latin America
11:10 - 11:40 AM	Jamal Deen	Smart Sensing and AI Towards Ubiquitous Healthcare
11:40 - 12:10 PM	Samar Saha	Invention of Field-Effect Device: How a Tiny Switch Sparked the Computing Revolution and Modern AI
12:10 - 1:10 PM	LUNCH	
1:15 - 3:00 PM	PANEL DISCUSSION	

PANEL DISCUSSION: A Roadmap for Building a Caribbean Semiconductor Industry

Since the Caribbean is not currently a major semiconductor manufacturing hub, discussions will focus on opportunities, barriers and strategic niches as opposed to large-scale manufacturing

PANEL MEMBERS: Fernando Guarin, Durga Misra, Arokia Nathan, Craig Ramlal, Arvind Singh and representatives from local industry and investment sectors

DAY 2

OCTOBER 6TH 2026

TECHNICAL PROGRAM

FET-100 POSTER CHALLENGE

THE TRANSISTOR AT 100:
SHAPING THE FUTURE OF INTELLIGENCE

Design a research poster that explores how transitor and electron-device technologies are transforming our world - and how they will shape the future of intelligence, innovation, industry and society.

WHAT YOU NEED TO DO

1. **Choose a topic**
   Select a theme related to the evolution, applications, impact or future of transistor/electron-device technologies

2. **Do your research**
   Explore current technologies, emerging innovations and real-world challenges and opportunities.

3. **Create your poster**
   Design a clear, engaging and informative poster that communicates your idea, findings and vision

4. **Present at FET-100**
   Display your poster on October 6th 2026 and present it to our panel of experts and attendees

WHO CAN PARTICIPATE?

University students from the Caribbean

COMPETITION FORMAT

- Individual participants or teams of 2-3 students
- Maximum of 10 participants
- No registration or participation fee
- Complementary access to the FET-100 event (October 5-6, 2026)

PRIZES

1st PLACE | 2nd PLACE | 3rd PLACE

POSTER GUIDELINES

Size: A1 (594 mm x 841 mm), Portrait

Format: Physical poster or online demonstration

Content: Must relate to the FET-100 theme - evolution, applications, impact or future of transistor/electron-device technologies.

Originality: Your work should be your own. Properly acknowledge any sources used.

IMPORTANT DATES

REGISTRATION DEADLINE
20th September 2026

POSTER PRESENTATION & JUDGING
6th October 2026

JUDGING CRITERIA

Technical Depth & Accuracy	30%
Originality & Creativity	25%
Clarity & Visual Communication	20%
Relevance to FET-100 Theme	15%
Future vision & Impact	10%

DISTINGUISHED LECTURERS

LECTURER / AFFILIATION	TITLE OF TALK
 **Fernando Guarin, Ph.D., IEEE Life Fellow** IEEE Division I Director (2025-2027); Past President, IEEE EDS; former Distinguished Member of Technical Staff, GlobalFoundries	100th Anniversary of the Transistor with an Overview of the Semiconductor Industry today and the opportunities for Latin America
 **Victor Grimblatt, Ph.D.** Executive Director, R&D Engineering, Design Technology Group, Synopsys; General Manager, Synopsys Chile.	Synopsys and its role in the development of Design Centres in Latin America
 **Samar Saha, Ph.D., IEEE Life Fellow** Chief Research Scientist, Prospicient Devices; Adjunct Faculty, Santa Clara University; Past President, IEEE EDS.	Invention of Field-Effect Device: How a Tiny Switch Sparked the Computing Revolution and Modern AI
 **Arokia Nathan, Ph.D.** Fellow, Darwin College, University of Cambridge; Professor at Shandong University, Qingdao, China	The Thin Film Transistor – Evolution and Applications
 **Durga Misra, Ph.D., IEEE Fellow** Professor, Department of Electrical and Computer Engineering, New Jersey Institute of Technology.	Engineering Energy Efficient Switching Devices for AI Hardware
 **Jamal Deen, Ph.D., IEEE Life Fellow** University Professor, Electrical and Computer Engineering, McMaster University, Hamilton, Ontario, Canada	Smart Sensing and AI Towards Ubiquitous Healthcare
 **Craig Ramlal, Ph.D.** Executive Director, AI Innovation Centre (AIIC)	The AI Innovation Centre (AIIC) in the Caribbean – Semiconductor Devices and Circuits Opportunities
 **Arvind Singh, Ph.D.** Professor, Head of Department of Electrical and Computer Engineering UWI, Deputy Director of AI Innovation centre (AIIC)	Expansion of UWI's Growth Plans in Semiconductor Education and Research