IEEE AP-S, IEEE NANOTECHNOLOGY COUNCIL, and IEEE AESS Student Branch Chapter, in association with Department of Electronics Engineering, IIT (BHU), Varanasi organized a Technical Talk on “Security issues in hardware designs” By Dr. Virendra Singh.

Date: 25th January, 2023 Time: 2:00 PM (IST) Venue: Committee Room, Electronics Engineering Department, IIT(BHU), Varanasi.

DATE
25-01-23

IEEE AP-S, IEEE NANOTECHNOLOGY COUNCIL, and IEEE AESS Student Branch Chapter,
in association with Department of Electronics Engineering, IIT (BHU), Varanasi

Topic Name: Security issues in hardware designs

By
Dr. Virendra Singh,
Professor,
Dept. of Electrical Engineering & Dept. of Computer Science & Engineering Indian Institute of Technology (IIT) Bombay Mumbai -400076, India

S.No.	Name	Signature
(1)	PRAVEEN SINGH RATHORE	
(2)	AJITESH	
(3)	RAHUL R. HINDUSTANI	
(4)	Deepak Ram	Deepak Ram
5)	Surya Protap Singh	
6)	HARSHIT SRIVASTAVA	
7)	PRANAV MITTAL	
8)	Ajit Mishra	
9)	Kunal Khare	
10)	Abhishek Rana	
11)	AMIT UPADHYAY	Amit upadhyay
12)	Vishal Singh	
13)	Jagadish	
14)	MEGHNA GUPTA	
15)	Rishabh Keshariani	
16)	Rahul kumar	
17)	Gummuluri Pavan kumar	G Pavankumar
18)	Paidi Raja sekhar	P Rajasekhar
19)	Neelesh prajapati	
20)	ARUN KR. SRIVASTAVA	
21	RAHUL CHAUHAN	
22	Prem Prakash Hangfam	
23.	SOUVIK BANIK	

S.No.	Name	Signature
24	Ravi	
25	Devvrat	Shukla
26		
27	Vivek Singh	
28	Amita Pal	
29.	Subrahmanya Kumar Bandyopadhyay	
30	Kapil Kushwaha	
31	Charma Arunkumar S	
32	Jaykumar Vaderiya	
33	Ashok Kumar	