IEEE Southeastern Michigan
Presents
Future Chips Roadmaps: CPUs, GPUs, FPGAs and Beyond

IEEE Southeastern Michigan
Electrical and Electronic Engineers Creating Our Future
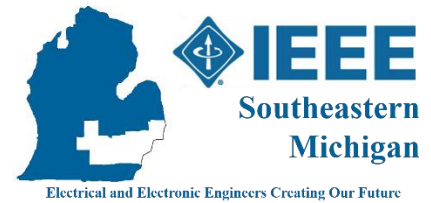
Standards and reference designs have helped establish many large scale compute installations and competition to maintain Moore's Law has become fierce. Now that we have seen Exascale capable system installed at Oak Ridge National Labs using these "commodity" designs, what is next for the mass computing market? This session will focus on how the technology that supercomputers and cloud use today will drive the next challenges for OEMs and also in the datacenters. Mechanical, thermal and electrical issues are being exacerbated by the intense focus to increase performance in every component of a system from CPUs to GPUs to Networking and beyond. You'll hear the strategy of how we will harness emerging technology for next-generation systems.

Bio:
Matthew Ziegler is the Director of HPC Architecture and Performance at Lenovo and responsible for defining Lenovo's systems and solutions strategy HPC and AI Deep Learning. Matthew joined IBM in 2001 where he worked as a HPC architect. Matthew later progressed to the role of Executive Architect in the System x-Product Marketing team at IBM before transitioning to Lenovo in 2014.

Pre-Registration Required!

https://events.vtools.ieee.org/m/353514

Quick Summary

- When:
  Date: April 27th, 2023
  Time: 04:30 – 5:30 PM (EST/EDT)

- Where:
  Online via Zoom/Webex (to be shared only after you have a confirmed registration)

- Audience: OPEN to ALL*

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