Bram Nauta was born in Hengelo, The Netherlands. In 1987 he received the M.Sc degree (cum laude) in electrical engineering from the University of Twente, Enschede, The Netherlands. In 1991 he received a Ph.D. from the same university on analog CMOS filters for very high frequencies. In 1991 he joined the Mixed-Signal Circuits and Systems Department of Philips Research, Eindhoven, the Netherlands. In 1998 he returned to the University of Twente as a full professor heading the new IC Design group. In 2014 he was nominated as a distinguished professor, and from 2016 until 2020, he served as chair of the electrical engineering department. In 2022 he co-founded "ChipTechTwente", a local ecosystem initiative with 50 semiconductor-related companies, partners, and knowledge institutions. His research interest is analog and radio frequency CMOS circuits.

He served as the Editor-in-Chief (2007-2010) of the IEEE Journal of Solid-State Circuits (JSSC), and was the 2013 program chair of the International Solid-State Circuits Conference (ISSCC). He served as the President of the IEEE Solid-State Circuits Society (2018-2019 term).

Also, he served as Associate Editor of IEEE Transactions on Circuits and Systems II (1997-1999), and of  IEEE Journal of Solid-State Circuits (2001-2006). He was on the Technical Program Committee of the Symposium on VLSI circuits (2009-2013) and served on the steering committee and program committee (1999-2017) of the European Solid-State Circuit Conference (ESSCIRC).  He served on the program committee (2003-2013) and the Executive committee (2007-2015, 2022-today) of the ISSCC, and in 2023, he joined the program committee of the Advances in Analog Circuit Design workshop series (AACD).

He is co-recipient of the ISSCC 2002 and 2009 "Van Vessem Outstanding Paper Award" and in 2023, he received an ISSCC Author-Recognition Award for its first 70 years, as a top 10 contributor. He served 2 terms as a distinguished lecturer of the IEEE Solid-State Circuits Society. In 2014 he received the "Simon Stevin Meester" award (500,000 Euros), the largest national prize in the Netherlands for achievements in engineering sciences. In 2019 he received an ERC (European Research Council) Advanced Grant (2.5 Million Euros, personal grant). In 2023, he received the inaugural "Dutch Innovation Award" and the NWO Stevin Prize (1.5 Million Euros), the largest national science prize in the Netherlands for "exceptional success in knowledge exchange and impact for society". Almost every year he is nominated for the ‘Best and Most Inspiring Teacher of the Year’ award in his university’s electrical engineering department.

He is a fellow of the IEEE and a member of the Royal Netherlands Academy of Arts and Sciences (KNAW).