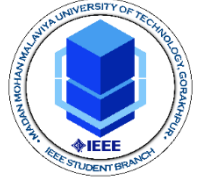




IEEE- Student Branch

MMMUT, GORAKHPUR



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REPORT

INTERNAL SMART INDIA HACKATHON-2024

Introduction: The Smart India Hackathon (SIH) is a prominent initiative by the Government of India, designed to tap into the creative abilities of students and motivate them to devise innovative solutions for real-world challenges. MMMUT values participation in such students.

Executive Summary: This report details the internal selection process conducted at Madan Mohan Malaviya University of Technology (MMMUT) for student participation in the esteemed **Smart India Hackathon (SIH)**. SIH is a nationwide initiative designed to encourage innovation and problem-solving skills among students, focusing on tackling challenges faced by the government and industry. In preparation for **SIH-2024**, MMMUT launched an internal selection, organised by **IEEE-SB MMUT** to identify the most creative and talented students to represent the university in the next stages of the competition. This report outlines the selection procedure, evaluation criteria, and the final list of students chosen. A total of **174 students (29 teams)** participated, consisting of **55 female** and **119 male** students from various branches and semesters. The internal selection event took place on **September 7, 2024**, at the MMMUT campus.

Selection Process: The internal selection process for the Smart India Hackathon at MMMUT was structured as follows:

1. Registration and Screening.
2. Ideation and Proposal Submission.
3. Evaluation.
4. Selection.

Criteria for Evaluation: The criteria used to evaluate students for the Smart India Hackathon were as follows:

1. **Innovation:** The originality and creativity of the proposed solution.
2. **Feasibility:** The practicality and likelihood of successfully implementing the solution.
3. **Relevance:** How well the proposed solution addresses the specific problem statement.
4. **Technical Proficiency:** The technical expertise and skillset of the team members.

5. Presentation Skills: The clarity and effectiveness with which the project proposal was presented.

Jury Details: Names and affiliations of the jury panel members:

- Dr. Rajan Mishra, Branch Counselor, IEEE-SB MMMUT Gorakhpur
- Dr. Rajendra Kumar Dwivedi, SPOC & COORDINATOR, MMMUT Gorakhpur
- Dr. Pradeep Muley, DEPUTY COORDINATOR, MMMUT Gorakhpur
- Dr. Shekhar Yadav, Assistant Professor, Department of Electrical Engineering, MMMUT Gorakhpur
- Dr. Dharmendra Kumar, Assistant Professor, Department of Electronics and Communication Engineering, MMMUT Gorakhpur
- Dr. Dheerendra Singh, Assistant Professor, Department of Mechanical Engineering, MMMUT Gorakhpur
- Dr. Sushil Kumar Saroj, Assistant Professor, Department of Computer Science and Engineering, MMMUT Gorakhpur

Conclusion: The internal selection process for SIH 2024 at MMMUT was a thorough and successful initiative to identify the university's most talented and innovative teams. These selected teams will represent MMMUT at the national level, showcasing their problem-solving abilities and contributing to the nation's progress through SIH 2024.

