# **Those Upsetting Ions-The Effects of Radiation on Electronics**

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Basic Radiation Damage Mechanisms

Transient Effects

History of Awareness of these effects

HCL/MGH Test Program









Atomic displacement damage in Si (mostly due to neutrons and Protons)

Minority carrier lifetime and mobility is reduced. This results in gain reduction in bipolor transistors & degradation of performance in LEDs and optical detectors.



## Trapped Charge in Oxides





## Trapped charge changes/degrades performance



Fig. 4.4 MOS characteristics: typical variation of (a) capacitance and (b) drain current with gate voltage, showing the shifts in flatband and threshold voltages due to trapped charge (no interface states).

## Transient Effects: Single Event Upset



## Single Event Latchup in CMOS Structures

### Silicon Controlled Rectifier (SCR) or Thyristor



### Parasitic equivalent structure in CMOS layout activated by ion strike



## Single Event Burnout (SEB) in Power MOSFETs

This effect was first noticed in high power components used in railway power supplies. The mechanism is described thusly;

"*SEB is triggered when a heavy ion passes through a power MOSFET biased in the off state (blocking a high drain-source voltage). Transient currents generated by the heavy ion turn on a parasitic BJT inherent to the device structure. Because of a regenerative feedback mechanism, collector currents in the BJT increase to the point where second breakdown sets in, creating a permanent short between the source and drain and rendering the MOSFET useless.*" (From G. H. Johnson, J. M. Palau, C. Dachs, K. F. Galloway, and R. D.Schrimpf, "A review of the techniques used for modeling single-event effects in power MOSFETs*," IEEE Trans. Nucl. Sci, vol. 43, no. 2, pp.546–560, Apr. 1996*

The mechanism is essentially the same when triggered by the charged secondaries produced by a neutron or proton strike on a silicon nucleus.

# Types of Radiation Damage/Effects:

1.Permanent or Long Term **Deterministic**

Displacement Damage

Trapped Charge

2. Transient **Stochastic**

Single Event Upset (SEU)

Single Event Latch Up (SEL)

Single Event \_\_\_\_ (Fill in the blank!)

#### SATELLITE ANOMALIES FROM GALACTIC COSMIC RAYS

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#### Introduction

Anomalies in communication satellite operation have been caused by the unexpected triggering of digital circuits. Although the majority of these events have been attributed to charge buildup from high temperature plasmas, (1,2) some of the events appear to be caused by another mechanism. The purpose of this paper is to investigate interactions with galactic cosmic rays as an additional mechanism.

The satellite anomalies studied were caused by the triggering of flip-flop circuits. The particular mechanism assumed for the cosmic ray interaction was the charging of the base-emitter capacitance of critical transistors to the turn-on voltage. The charge is produced by the dense ionization track of an energetic, high-Z cosmic ray.

The following sections describe the determination of the number of sensitive transistors, the charge collection efficiency, the transistor parameters, and the energy spectrum of the penetrating cosmic rays. All these quantities were required as input data for a computer code called CRAC, developed for this problem. The code was used to calculate the solid angle in which cosmic rays supplied sufficient energy to turn on the transistors. The theoretical event rate was then calculated and compared with satellite data.



J-K Flip-Flop Circuit with Transistor States Figure 1.



#### IEEE Transactions on Nuclear Science, Vol. NS-26, No. 6, December 1979

#### SOFT ERRORS INDUCED BY ENERGETIC PROTONS

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#### 1. Introduction

There has been considerable recent interest in the soft error phenomena in semiconductor memory devices. <sup>1-5</sup> Soft memory errors take the form of anomolous changes in the information stored in a memory device flown in space without observable damage to the device itself. Soft errors were first observed in bipolar digital components -J-K flip-flops. <sup>1</sup> The soft-error rates in satellite systems have increased significantly recently, presumably because of the increased use of largescale integrated (LSI) devices. $2-5$  The decrease in volume of a sensitive element implies a corresponding decrease in the stored charge and in the number of ion pairs necessary to induce a soft error.

The primary mechanism for the soft errors in LSI devices is believed to be the passage of a heavy cosmic-ray nucleus through a memory storage element where the energy deposited by the particle traversing the element creates a sufficient number of electron-hole pairs at or near the depletion region to neutralize the stored charge.<sup>1-5</sup> According to this hypothesis, all soft errors should be one to zero for memory elements in which the data is stored true and zero to one for elements which store information in complement form. Any devices of comparable geometry on the microscopic level should have equal probability of exhibiting soft errors. According to the primary mechanism LSI devices should be sensitive to heavily ionizing particles that have values of linear energy transfer (LET) above some threshold value.

Nuclear interactions provide a possible alternative mechanism for the soft errors. The secondary particles (mostly protons and alphas) emerging from inelastic nuclear interactions may ionize a sufficient number of atoms along their trajectories to change the state of a nearby memory element. May and Woods have recently observed soft errors induced by alpha particles,<sup>2</sup> and Yaney et al report peak sensitivity for a maximum alpha energy of 3-4 MeV.<sup>4</sup> The recoiling nucleus can provide considerable local ionization loss. Bradford<sup>5</sup> has pointed out that nuclear reactions can contribute to the soft errors, especially for VLSI where the threshold energy that must be deposited for a soft error is expected to be below 1 MeV. Preliminary calculations by Farrell and McNulty<sup>6</sup> indicate that the nuclear recoil in inclastic

# 2. Experimental Procedures

Two types of 4K x 1 dynamic RAM, Intel C2107B and National semiconductor MM-5280, were studied in these investigations. Both devices came in ceramic packages with metal lids, which were removed for low energy proton exposures. The energetic proton exposures were carried out at the Harvard. Cyclotron and included energies of 18, 32, 51, 91, and 130 MeV. The energy of the protons was controlled by inserting lucite decreders in the This introduced considerable spread in beam. the beam energy for the lower energies, (FWHMs were  $\sim$  17, 14, 10, 4 MeV for the 18, 32, 51, and 130 MeV beams, respectively.) The 0.95, 1.3, and



## IBM Experiments to show that terrestrial neutrons are a significant source of failures (1982-1988)



From "SER-History, Trends and Challenges", James F. Ziegler and Helmut Puchner (Cypress)



Harvard Cyclotron circa 1950

## Andreas ("Andy") Koehler



1953



~1980



2002



Not much changed for the cyclotron equipment over the years





Or with the working environment





"physics" beamline (D) from HCL at MGH





## Chip under test illuminated by proton beam

New (equivalent) beamline at MGH



## Dosimetry control unit from HCL at MGH with additions

## Recent replacement units at MGH







System at MGH for reducing energy spread of beams transported to the treatment rooms



Drawing from Ernest Lawrence's original patent for the Cyclotron (1934)





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From "**The Harvard University 95 Inch Cyclotron** Design, Construction, and Preliminary Operating Instructions"

July 1950, Office of Naval Research

One issue is "recombination" signal loss in the monitor ion chamber. If the flux density is too high some of the ion/electron pairs will re-combine before they can be collected in the signal foil. Narrow gap between the foils is best way to avoid this. HCL ion chambers were built with very narrow gap due to low duty cycle/high instantaneous beam current.





Precision Al absorber set from HCL

Measurements of areal density (weight/area) of pieces from 1967







From E. W. Cascio and S. Sarkar "A Solar Flare Simulation Wheel for the Radiation Test Beamline at The Francis H. Burr Proton Therapy Center", IEEE Transactions on Nuclear Science, Vol. 55, NO. 6 (2008)



Markup plan for experimental/test room post Burr 2.0