



CHARUSAT
CHAROTAR UNIVERSITY OF SCIENCE AND TECHNOLOGY



CHARUSAT
IEEE Student Branch

"A Student Chapter of the IEEE Circuits and Systems Society"

CAS
IEEE CIRCUITS AND SYSTEMS SOCIETY

IEEE CASS Student Branch Chapter - CHARUSAT in Collaboration with ChipXplorer - VLSI Club
Presenting

iChip - 3.0

"Code2Chip: FPGA Innovation Hackathon"

Under

CASS CONVERGENCE 2025

Challenge Rounds

1

Debug & Conquer:
Error solving in Verilog code

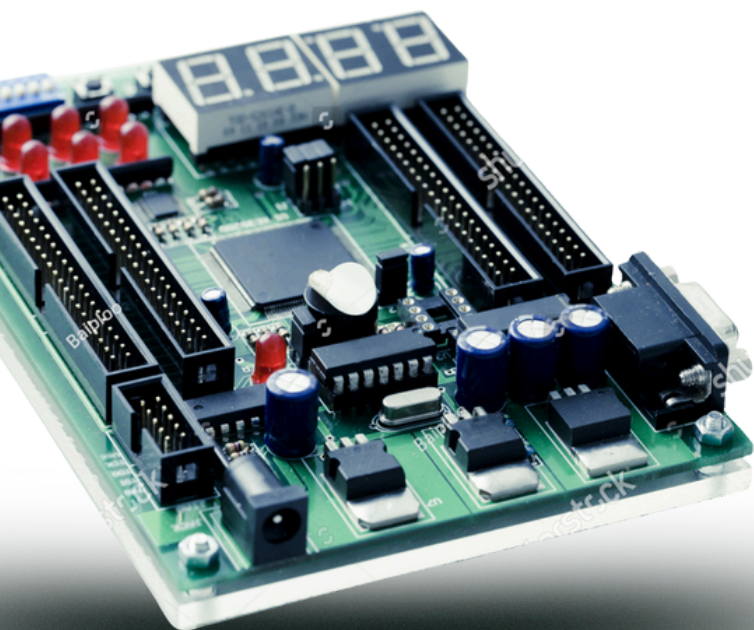
2

CodeCraft:
Design and code digital circuits
using Verilog

3

BoardQuest:
Implementing the designed
circuits on FPGA boards

"7 HOURS TO DEBUG, DESIGN & DEPLOY ON FPGA."



Scan to Register &
Win Exciting
Prizes!



Date & Time :
11th October, 2025
09:30 AM to 04:30 PM



Venue:
VLSI Design&Embedded Lab2,
EC Dept., A6 Building, CSPIT

Event Coordinator

Dr. Arpita Patel

Advisor, IEEE CASS SBC

Coordinator, ChipXplorer – VLSI Club